

New 2-D Quadrantal- and Diagonal-Symmetry Filter Architectures Using Delta Operator

Pei-Yu Chen and Lan-Da Van

Department of Computer Science,

National Chiao Tung University, Hsinchu, Taiwan

e-mail: pychen@viplab.cs.nctu.edu.tw ; ldvan@cs.nctu.edu.tw

I. H. Khoo and Hari C. Reddy, *IEEE Fellow*

Department of Electrical Engineering, California State

University, Long Beach, USA

e-mail: I-Hung.Khoo@csulb.edu ; hreddy@csulb.edu

Abstract—In this paper, we propose two new two-dimensional (2-D) IIR filter architectures with quadrant and diagonal magnitude symmetry using delta operator. These filter architectures can attain lower coefficient sensitivity in comparison with the traditional shift-operator formulation (i.e., the conventional z -domain filters). The IIR filter structures presented not only achieve lower coefficient sensitivity without sacrificing the number of multipliers, but in fact reducing the number of multipliers by 36.36% compared with the previous corresponding quadrant symmetry filter structures using delta operator with order $N=3$.

I. INTRODUCTION

Two-dimensional (2-D) digital filters have been extensively employed in many research and practical digital signal processing (DSP) applications [1, 2, 3]. In the past decade, the symmetry properties applied to digital filters are widely discussed in [4-11]. From these studies, the number of multipliers can be potentially decreased if the proper symmetry is adopted. Concerning the hardware implementation, high speed 2-D digital filters [12] and the related symmetry digital filter architectures [13-17] have been devised in an application-specific integrated circuit (ASIC) approach. The design of quadrant symmetry filter structure using delta operator, where the structure combined the advantages of delta operator and quadrant symmetry was given in [15]. It is known that delta operator based digital filter can offer better numerical accuracy and lower coefficient sensitivity in narrow-band filter designs compared with that in traditional z -domain, and further the quadrant symmetry can be applied to reduce the number of multipliers in a digital filter. However, how to improve the quadrant symmetry filter architecture [15] and devise other symmetry filter architectures have not yet been explored. Thus, it is our motivation to propose an improved quadrant symmetry filter architecture and a new diagonal symmetry filter architecture with lower coefficient sensitivity in an ASIC

approach. In this paper, the preliminaries of 2-D filter transfer functions and symmetry using delta operator are briefly introduced in Sections II. In Section III and IV, 2-D quadrant and diagonal-symmetry filter architecture using delta operator with separable denominator transfer function are presented. Next, the comparison and discussion is given in Section V. Finally, the conclusion is marked in Section VI.

II. PRELIMINARIES

The general 2-D filter transfer function using delta operator in r domain is released in [15] as follows.

$$H(\gamma_1, \gamma_2) = \frac{Y(\gamma_1, \gamma_2)}{X(\gamma_1, \gamma_2)} = \frac{\sum_{i=0}^{N_1} \sum_{j=0}^{N_2} c_{ij} \gamma_1^{-i} \gamma_2^{-j}}{1 - \sum_{i=0}^{N_1} \sum_{j=0}^{N_2} d_{ij} \gamma_1^{-i} \gamma_2^{-j}} \quad (1)$$

, where the relationship between transfer function $H(z_1, z_2)$ in the z -domain and transfer function $H(\gamma_1, \gamma_2)$ in the γ -domain is

$$H(\gamma_1, \gamma_2) = H(z_1, z_2) \Big|_{z_i = (1 + T_i \gamma_i), i=1,2} \quad (2)$$

Adopting the symmetry features, the transfer function with separable denominator can be expressed in (3).

$$H(\gamma_1, \gamma_2) = \frac{Y(\gamma_1, \gamma_2)}{X(\gamma_1, \gamma_2)} = \frac{\sum_{i=0}^{N_1} \sum_{j=0}^{N_2} c_{ij} \gamma_1^{-i} \gamma_2^{-j}}{\left(1 - \sum_{i=1}^{N_1} d_{i0} \gamma_1^{-i}\right) \left(1 - \sum_{i=1}^{N_2} d_{0j} \gamma_2^{-j}\right)} \quad (3)$$

The separable denominator transfer function has following advantages. First, it is easy to check and maintain the stability of the two 1-D polynomials by solving the poles. Second, fewer hardware multipliers are needed in realization. Without loss of generality, the filter order is $N_1=N_2=N$ and an image size is $M \times M$ throughout this paper.

III. 2-D QUADRANTAL SYMMETRY FILTER ARCHITECTURE USING DELTA OPERATOR

In this section, the quadrantal symmetry filter with delta operator will be discussed. The property of quadrantal property mentioned in [4, 6-11] is that the magnitude squared function is

$$F(\theta_1, \theta_2) = F(-\theta_1, \theta_2) = F(\theta_1, -\theta_2) = F(-\theta_1, -\theta_2), \forall(\theta_1, \theta_2) \quad (4)$$

$$\text{,where } F(\theta_1, \theta_2) = P(\gamma_1, \gamma_2) \cdot P\left(\frac{-\gamma_1}{1+T\gamma_1}, \frac{-\gamma_2}{1+T\gamma_2}\right) \Bigg|_{\gamma_i = \frac{e^{j\theta_i} - 1}{T}}, i=1,2$$

where T denotes the sampling period. The transfer function of the quadrantal symmetry filter using delta operator is repeated in (5) [15].

$$H(\gamma_1, \gamma_2) = \frac{Y(\gamma_1, \gamma_2)}{X(\gamma_1, \gamma_2)} = \frac{\sum_{i=0}^N \sum_{j=0,2,4,\dots}^N c_{ij} \gamma_1^{-i} (\gamma_2^{-2} + \gamma_2^{-1})^{j/2}}{\left(1 - \sum_{i=1}^{N_1} d_{i0} \gamma_1^{-i}\right) \left(1 - \sum_{i=1}^{N_2} d_{0j} \gamma_2^{-j}\right)} \quad (5)$$

The quadrantal symmetry filter structure using delta operator has been released in [15]. However, the numbers of multipliers and adders have not yet been further reduced. Using the coefficient matrix distribution of the quadrantal symmetry property for $N=3$ as shown in Fig. 1, the numerator coefficients in square box can be shared.

$$\begin{matrix} \gamma_1^{-0} & \gamma_1^{-1} & \gamma_1^{-2} & \gamma_1^{-3} \\ \gamma_2^{-0} & \begin{bmatrix} c_{00} & c_{10} & c_{20} & c_{30} \\ c_{02} & \boxed{c_{12}} & \boxed{c_{22}} & \boxed{c_{32}} \end{bmatrix} \\ \gamma_2^{-1} & \begin{bmatrix} c_{02} & c_{12} & c_{22} & c_{32} \end{bmatrix} \\ \gamma_2^{-2} & \begin{bmatrix} c_{02} & c_{12} & c_{22} & c_{32} \end{bmatrix} \\ \gamma_2^{-3} & \begin{bmatrix} 0 & 0 & 0 & 0 \end{bmatrix} \end{matrix}$$

Fig. 1. Coefficient matrix distribution of the quadrantal symmetry.

Corresponding to (5) and Fig. 1, the block diagram of an improved quadrantal symmetry filter architecture using delta operator is shown in Fig. 2. In Fig. 2, the black box denotes one delay element z^{-1} , and the stripe box is the shift register, which realizes $z_I^{-1} = z^{-M}$ with size of M [14]. It is noted that the proposed separable denominator filter structure is different from that proposed in [15], where the shifted delay location in Fig. 2 using the register splitting method [12] can attain the multiplier sharing for diagonal symmetry filter architecture. The 2-D FIR filter architecture can be simplified in Fig. 3.

IV. 2-D DIAGONAL SYMMETRY FILTER ARCHITECTURE USING DELTA OPERATOR

The 2-D diagonal symmetry filter structure using delta operator will be more complicated if the regular modules [15] are used. According to the property mentioned in [8], the polynomial in r domain is $P(r_1, r_2) = P(r_2, r_1)$. The transfer function of the diagonal symmetry filter in r domain is

$$H(\gamma_1, \gamma_2) = \frac{\sum_{i=0}^N c_{ii} \gamma_1^{-i} \gamma_2^{-i} + \sum_{i=1}^{N-1} \sum_{j=i+1}^N c_{ij} (\gamma_1^{-i} \gamma_2^{-j} + \gamma_1^{-j} \gamma_2^{-i})}{\left(1 - \sum_{i=1}^{N_1} d_{i0} \gamma_1^{-i}\right) \left(1 - \sum_{i=1}^{N_2} d_{0j} \gamma_2^{-j}\right)} \quad (6)$$

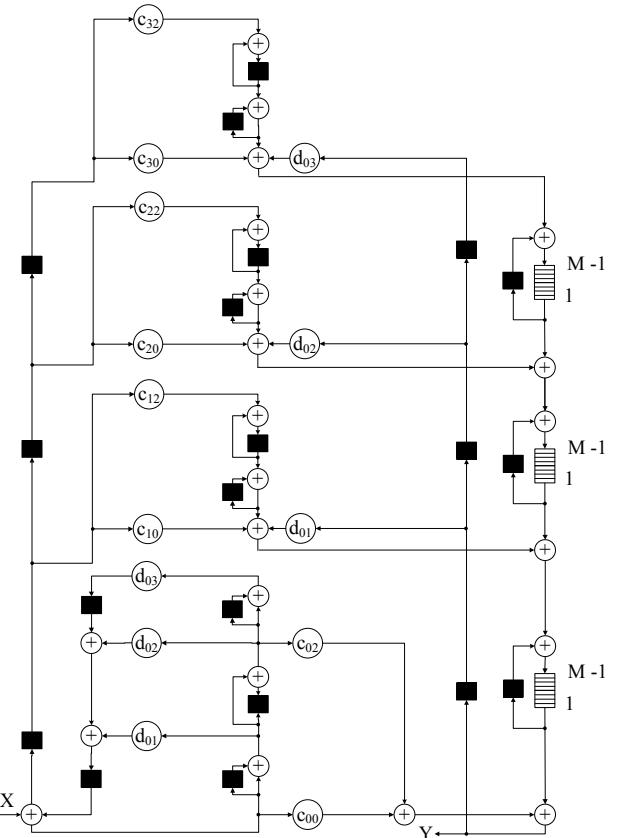


Fig. 2. 2-D IIR filter architecture with quadrantal symmetry using delta operator for $N=3$.

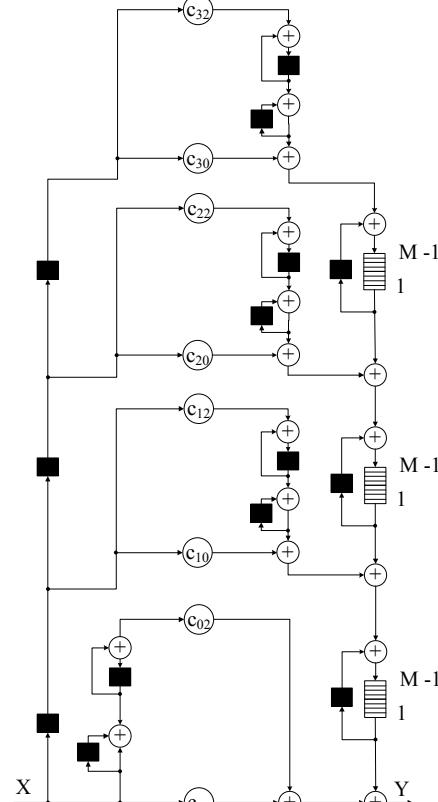


Fig. 3. 2-D FIR filter architecture with quadrantal symmetry using delta operator for $N=3$.

$$\begin{matrix} \gamma_1^{-0} & \gamma_1^{-1} & \gamma_1^{-2} & \gamma_1^{-3} \\ \gamma_2^{-0} & \boxed{c_{00}} & c_{01} & c_{02} & c_{03} \\ \gamma_2^{-1} & \boxed{c_{01}} & c_{11} & c_{12} & c_{13} \\ \gamma_2^{-2} & \boxed{c_{02}} & \boxed{c_{12}} & c_{22} & c_{23} \\ \gamma_2^{-3} & \boxed{c_{03}} & \boxed{c_{13}} & \boxed{c_{23}} & c_{33} \end{matrix}$$

Fig. 4. Coefficient matrix distribution of the diagonal symmetry.

Using the coefficient matrix distribution of the diagonal symmetry property for $N=3$ as shown in Fig. 4, the numerator coefficients in square box can be shared. Thus, a new 2-D diagonal symmetry filter architecture using delta operator is shown in Fig. 5. The 2-D FIR filter architecture can be simplified in Fig. 6.

V. COMPARISON AND DISCUSSION

In this section, the architecture analysis and comparison results are shown in Table 1 in terms of the numbers of multipliers and adders in quantitative way, and the coefficient sensitivity in qualitative way. It is well-known that symmetry presented in the frequency responses of 2-D filters can be used to reduce the number of multipliers [14]. This work proposes one improved 2-D quadrantal-symmetry filter architecture and one new 2-D diagonal-symmetry filter architecture using delta operator for $N=3$. In similar way, the comparison of two new 2-D FIR filter architectures with quadrant and diagonal symmetries using delta operator to other literatures are presented in Table 2 by setting coefficients in denominator as zero. In Table 1, compared with conventional quadrant, and diagonal symmetry filter structures in [14], the proposed 2-D IIR filter structures using delta operator can achieve lower coefficient sensitivity without sacrificing the number of multipliers. In terms of number of multipliers, the proposed 2-D quadrant symmetry filter structure using delta operator with $N=3$ can achieve the reduction by 36.36% compared with that in [15]. In terms of adders, it is known that the area of an adder is much less than that of a multiplier. In order to achieve fair comparison, the n -bit adder can be equivalently evaluated as $1/n$ $n \times n$ -bit multiplier using array multiplier approach [18]. According to the hardware implementation in [14], 10x10-bit multiplier and 10-bit full-adder with two inputs are reasonable assumptions to realize this evaluation. In Table 1 and Table 2, we can easily observe that the reduction of number of multipliers of the filter is still much larger than the increase of number of equivalent multipliers (i.e., the number of adders with two inputs).

VI. CONCLUSION

In this paper, two new symmetry filter architectures using delta operator are explored. Using these two new symmetry filter architectures, not only the fewer number of multipliers but also the lower coefficient sensitivity can be attained.

ACKNOWLEDGEMENT

This work was supported in part by the Ministry of Science and Technology (MOST) under Grant MOST 106-2221-E-009-028-MY3 and MOST 103-2221-E-009-099-MY3.

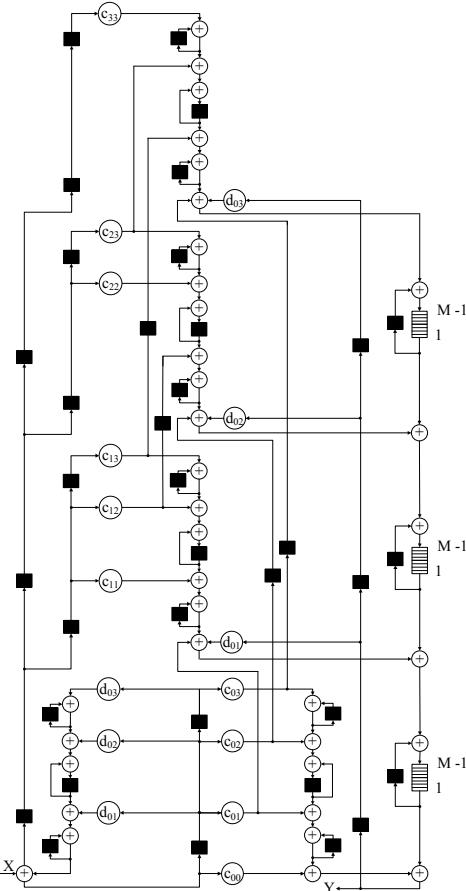


Fig. 5. 2-D IIR filter architecture with diagonal symmetry using delta operator for $N=3$.

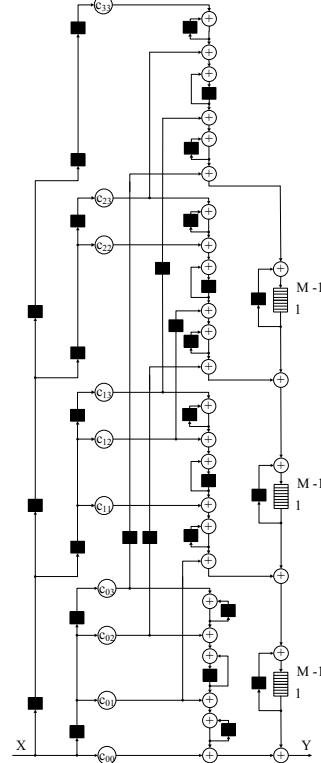


Fig. 6. 2-D FIR filter architecture with diagonal symmetry using delta operator for $N=3$.

REFERENCES

- [1] D. E. Dudgeon and R. M. Mersereau, *Multidimensional Digital Signal Processing*. Englewood Cliffs, NJ: Prentice-Hall, 1984.
- [2] M. A. Sid-Ahmed, *Image Processing: Theory, Algorithms, and Architectures*. NY: McGraw-Hill, 1995.
- [3] A. Madanayake, C. Wijenayake, D. G. Dansereau, T. K. Gunaratne, L. T. Bruton, and S.B. Williams, "Multidimensional (MD) circuits and systems for emerging applications including cognitive radio, radio astronomy, robot vision and imaging", *IEEE Circuits and Systems Magazine*, vol. 13, no. 1, pp. 10-43, 2013.
- [4] M. N. S. Swamy and P. K. Rajan, Symmetry in 2-D filters and its application," in *Multidimensional Systems: Techniques and Applications* (S.G. Tzafestas, Ed.). New York: Marcel Dekker, 1986, Ch. 9.
- [5] R. H. Middleton and G. C. Goodwin, "Improved finite word length characteristics in digital control using delta operators," *IEEE Trans. Automatic Control*, vol. 31, pp. 1015-1021, Nov. 1986.
- [6] H. C. Reddy, P.K. Rajan, G. S. Moschytz, and A. R. Stubberud. "Study of various symmetries in the frequency response of two-dimensional delta operator formulated discrete-time Systems," in *Proc. IEEE ISCAS*, vol. 2, pp. 344-347, May 1996.
- [7] H. C. Reddy, I. H. Khoo, G. S. Moschytz and A. R. Stubberud, "Theory and test procedure for symmetries in the frequency response of complex two-dimensional delta operator formulated discrete-time systems," in *Proc. IEEE ISCAS*, vol. 4, pp. 2373-6, Jun. 1997.
- [8] I. H. Khoo, H. C. Reddy, P. K. Rajan, "Delta operator based 2-D filter design using symmetry constraints," in *Proc. IEEE ISCAS*, vol. 2, pp. 781-784, May 2001.
- [9] H. C. Reddy, I. H. Khoo and P. K. Rajan, "2-D symmetry: theory and filter design applications," *IEEE Circuits and Systems Magazine*, vol. 3, pp. 4-33, 2003.
- [10] I. H. Khoo, H. C. Reddy, and P. K. Rajan, "Symmetry study for delta-operator-based 2-D digital filters," *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 53, no. 9, pp. 2036-2047, Sep. 2006.
- [11] I. H. Khoo, H. C. Reddy, P. K. Rajan, "Unified theory of symmetry for two-dimensional complex polynomials using delta discrete-time operator," *Multidimensional Systems and Signal Processing*, 22, pp. 147-172, 2011.
- [12] L. D. Van, "A new 2-D systolic digital filter architecture without global broadcast," *IEEE Trans. VLSI Syst.*, vol. 10, no. 4, pp. 477-486, Aug. 2002
- [13] P. Y. Chen, L. D. Van, H. C. Reddy and C. T. Lin, "A new VLSI 2-D diagonal-symmetry filter architecture design," in *Proc. IEEE APCCAS*, Macao, China, pp. 320-323, Nov. 2008.
- [14] P. Y. Chen, L. D. Van, I. H. Khoo, H. C. Reddy and C. T. Lin, "Power-Efficient and Cost-Effective 2-D Symmetry Filter Architectures," *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 58, no. 1, pp. 112-125, Jan. 2011.
- [15] I. H. Khoo, H. C. Reddy, L. D. Van, and C. T. Lin, "General formulation of shift and delta operator based 2-D VLSI filter structures without global broadcast and incorporation of the symmetry," *Multidimensional Systems and Signal Processing*, 25, pp. 795-828, 2014.
- [16] P. Y. Chen, L. D. Van, H. C. Reddy, and I. H. Khoo, "Area-efficient 2-D digital filter architectures possessing diagonal and four-fold rotational symmetries," in *Proc. ICICS*, Taiwan, Dec., 2013.
- [17] P. Y. Chen, L. D. Van, H. C. Reddy, and I. H. Khoo, "New 2-D filter architectures with quadrantal symmetry and octagonal symmetry and their error analysis," accepted in *IEEE MWSCAS* 2017.
- [18] N. H. E. Weste and D. Harris, *CMOS VLSI Design: A Circuit and Systems Perspective*, Addison Wesley, 2005, Ch. 10.

Table 1. Comparison of different 2-D IIR filters architectures with the order N .

Works	# of Multipliers	# of Multipliers for $N=3$		# of Adds with two inputs for $N=3$		Operator	Coefficient Sensitivity ^{*3}
		#	%	#	Equivalent # of 10x10-bit mul.		
[12]	$2(N+1)^2 - 1$	31 for Gen	100%	30	3.0	Shift	●
[14] Type-1	$\left\{ \frac{1}{2}(N+1)^2 + \frac{v}{2}(N+1) + 2N, \frac{1}{2}(N+1)^2 + \frac{5}{2}N + \frac{1}{2} \right\}$ ^{*1*2}	{14, 16}	{45.16%, 51.61%}	{21, 21}	{2.1, 2.1}	Shift	●
[15]	$(N+1)^2 + 2N$	22 for Qua	70.97%	55	5.5	Delta	●●
[17]	$\frac{1}{2}(N+1)^2 + \frac{v}{2}(N+1) + 2N$ ^{*2}	14 for Qua	45.16%	21	2.1	Shift	●
This work	$\begin{cases} \frac{1}{2}(N+1)^2 + 2N, & \text{for } N \text{ is odd} \\ (N/2+1)(N+1) + 2N, & \text{for } N \text{ is even} \end{cases}$ ^{*1}	{14, 16}	{45.16%, 51.61%}	{27, 39}	{2.7, 3.9}	Delta	●●

*1: {a, b} = {for quadrantal, for diagonal}. *2: $v=(N+1) \bmod 2$. *3: More block circles indicate better numerical accuracy.

Table 2. Comparison of different 2-D FIR filters architectures with the order N .

Works	# of Multipliers	# of Multipliers for $N=3$		# of Adds with two inputs for $N=3$		Operator	Coefficient Sensitivity ^{*3}
		#	%	#	Equivalent # of 10x10-bit mul.		
[12]	$(N+1)^2$	16 for Gen	100%	15	1.5	Shift	●
[14] Type-1	$\left\{ \frac{1}{2}(N+1)^2 + \frac{v}{2}(N+1), \frac{1}{2}(N+1)^2 + \frac{1}{2}N + \frac{1}{2} \right\}$ ^{*1*2}	{8, 10}	{50%, 62.5%}	{15, 15}	{1.5, 1.5}	Shift	●
[15]	$(N+1)^2$	16 for Qua	100%	30	3.0	Delta	●●
[17]	$\frac{1}{2}(N+1)^2 + \frac{v}{2}(N+1)$ ^{*2}	8 for Qua	50%	15	1.5	Shift	●
This work	$\begin{cases} \frac{1}{2}(N+1)^2, & \text{for } N \text{ is odd} \\ (N/2+1)(N+1), & \text{for } N \text{ is even} \end{cases}$ ^{*1}	{8, 10}	{50%, 62.5%}	{18, 30}	{1.8, 3.0}	Delta	●●

*1: {a, b} = {for quadrantal, for diagonal}. *2: $v=(N+1) \bmod 2$. *3: More block circles indicate better numerical accuracy.